

Question # 1: (25 points)

Part (a)

Show or list all of the dependencies in this program. For each dependency, indicate which instructions *and* register are involved. (5 points)

```

add $8, $5, $5
add $2, $5, $8
sub $3, $8, $4
add $2, $2, $3
    
```

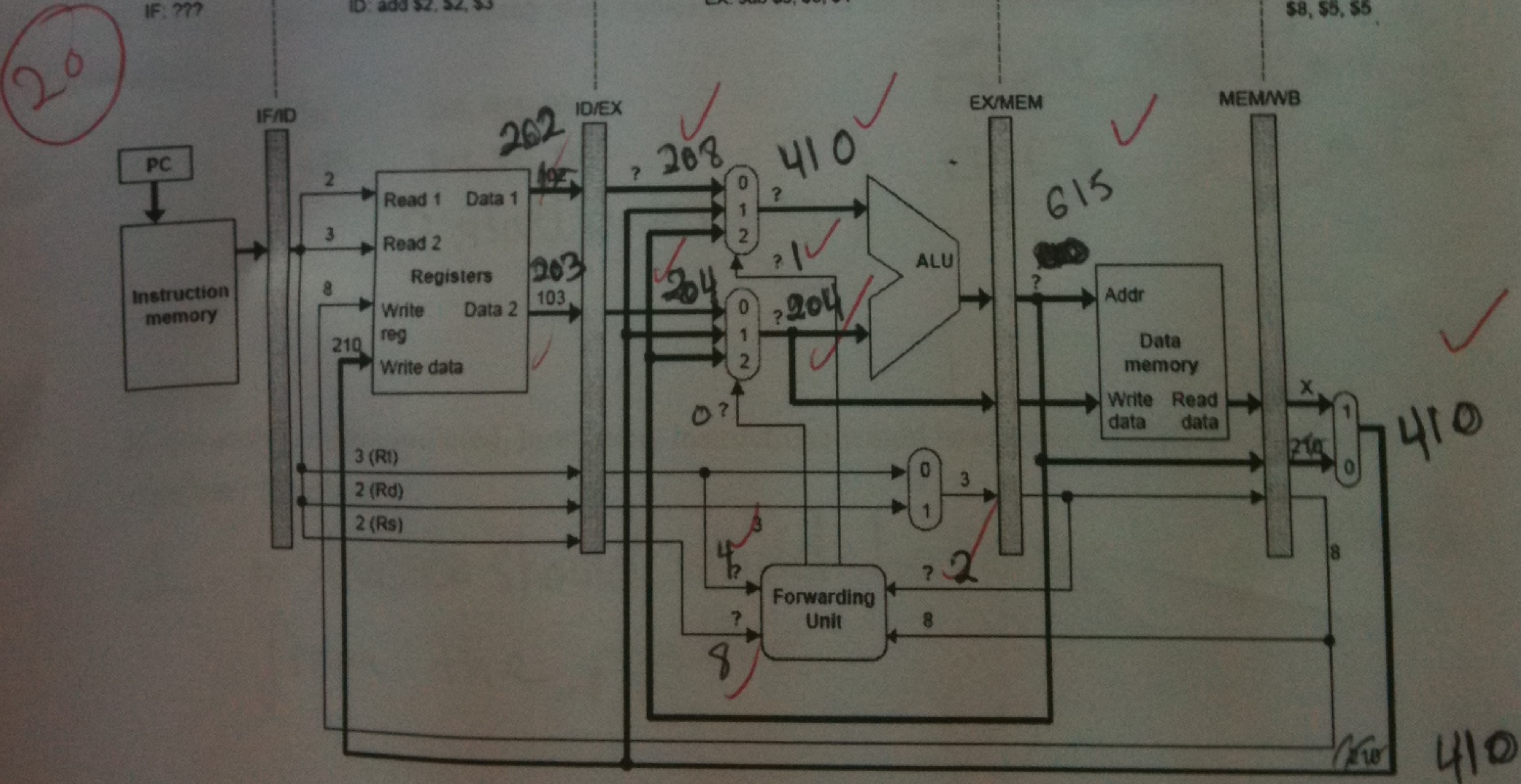
(5)

Part (b)

The pipelined datapath on the next page shows the fifth cycle of executing this program, including values for several of the stages. Fill in the ten remaining values, marked with a ? symbol, in the EX and MEM stages. (20 points; 2 points each)

N.B.: • Assume that registers initially contain their number plus 200: \$2 contains 202, \$8 contains 208, etc.

- Write your answers directly on the diagram, but write clearly.
- Show decimal values, and write 'X' for any numbers that cannot be determined.



Question # 2: (20 points)

IPG	FET	ROT	EXP	REN	WLD	REG	EXE	DET	WRB
1	2	3	4	5	6	7	8	9	10

One CPU manufacturer has proposed the 10-stage pipeline above for a 500MHz (2ns clock cycle) machine. Here are the correspondences between this and the MIPS pipeline:

- Instructions are fetched in the FET stage.
- Register reading is performed in the REG stage.
- ALU operations *and* memory accesses are both done in the EXE stage.
- Branches are resolved in the DET stage.
- WRB is the writeback stage.

Part (a)

How much time is required to execute one million instructions on this processor, assuming there are no dependencies or branches in the code? (5 points)

~~Time = 1000000 * 10 * 2 * 10^-9 = 2 * 10^-9~~

$$\text{Time} = \frac{1000000 \times 10 \times 50}{2 \times 10^{-9}} = 2.5 \times 10^{17}$$

3.5

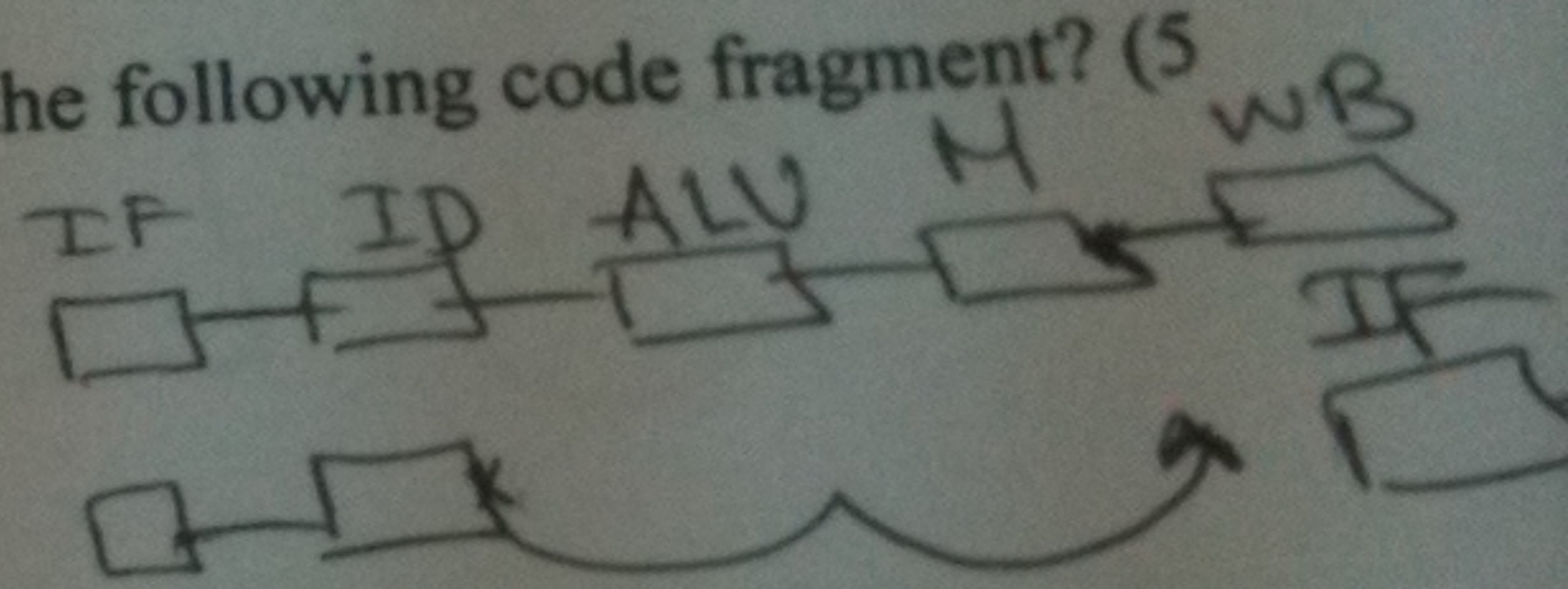
Part (b)

Without forwarding, how many stall cycles are needed for the following code fragment? (5 points)

```
lw    $t0, 0($a0)
add   $v1, $t0, $t0
```

2 stall cycles

5



Part (c)

If a branch is miss-predicted, how many instructions would have to be flushed from the pipeline? (5 points)

1 instruction should would have to be flushed from the pipeline

2

Part (d)

Assume that a program executes one million instructions. Of these, 15% are load instructions which stall, and 10% of the instructions are branches. The CPU predicts branches correctly 75% of the time. How much time will it take to execute this program? (10 points)

~~CPU time = 1000000 * 1.15 = 1150000~~

~~CPU time = 1000000 * 1.15 = 1150000~~

Execution time = $Exec_{Ideal} + Exec_{Stall}$

Memory Hierarchy

Question # 3: (15 points)

Assume a processor has a byte addressable memory of 512 Kbytes and a cache block size of 4 Bytes. If the student architect was to design a direct-mapped cache of size 4 Kbytes, what should the following be:

a) The size of the memory address (4 points)

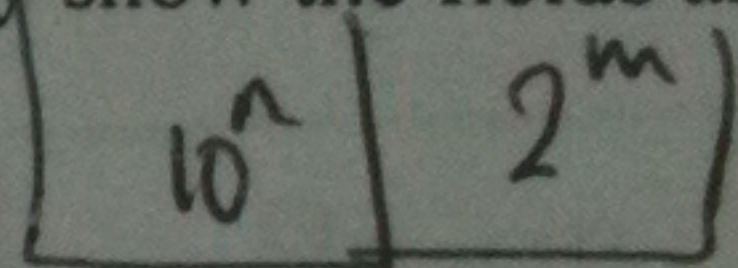
~~512 Kbytes~~

$512 \times 2^{10} = 2^9 \times 2^{10} = 2^{19}$ bytes

19 bits

4

b) How is the address partitioned for the cache design? Clearly show the fields and summarize the function of each field (4 points)



$2 - (10 + 2)$
20

4

$\frac{4 \times 10^{10}}{4} = 10^{10}$

index = $10 = n$

offset = $2 = m$

c) Which one of these fields is also stored in cache and why? (3 points)

Tag is stored in cache in order to validate if the data is correct and it matches

3

d) What is the total size in bytes for each cache? (4 points)

$$\begin{aligned} \text{Total cache size} &= 2^m (m \times 32 + (32 - m - n - 2) + 1) \\ &= 2^{10} ((32 \times 2) + (32 - 12) + 1) \\ &= 85 \text{ KB} \end{aligned}$$

Question # 4: (10 points)

A cache currently has an average memory access time of 12 ns, a hit time of 8 ns, and a hit rate of 90%. If doubling the size of the cache increases the hit time by 25% and increases the hit rate to 96%, what will the new average memory access time (AMAT) be?

Memory access time of 12 ns

hit time = 8 ns

hit rate = 90%

8.5

$$\text{AMAT} = \text{hit time} \times \text{hit rate} + (\text{miss rate} \times \text{miss penalty})$$

$$\frac{\text{AMAT}}{\text{hit time} \times \text{hit rate}} = \text{miss rate} \times \text{miss penalty}$$

$$\text{miss rate} \times \text{miss penalty} = \frac{12 \times 10^{-9}}{8 \times 10^{-9} \times 90 \times 10^{-2}}$$

increase

$$100\% + 25\% = 1.25$$

$$= 1.67$$

$$\text{new AMAT} = (1.25 \times 8 \text{ ns} \times 0.96) + 1.67$$

$$= 12.53 \text{ ns}$$